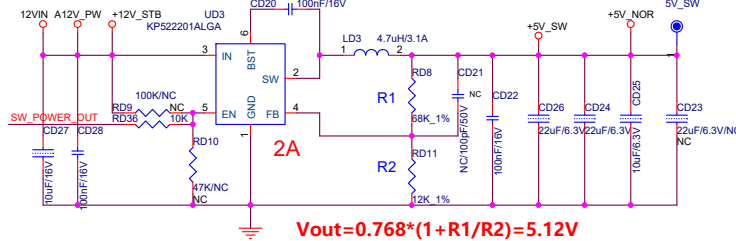
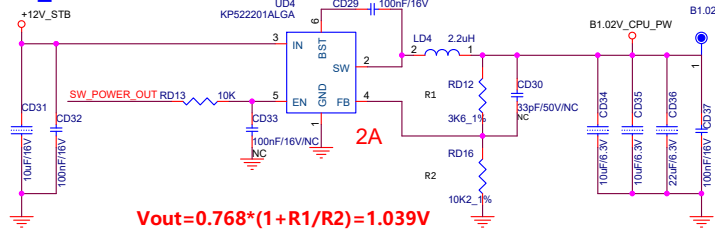


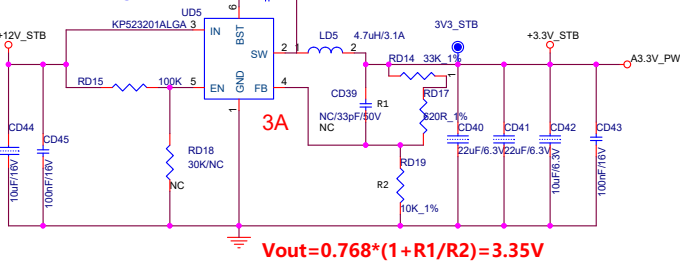
5V SW Power



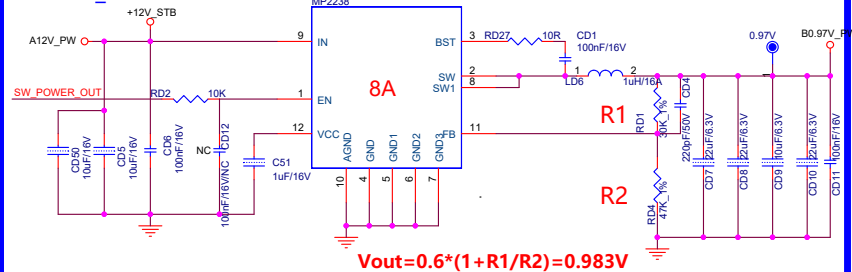
CPU_1.02V POWER



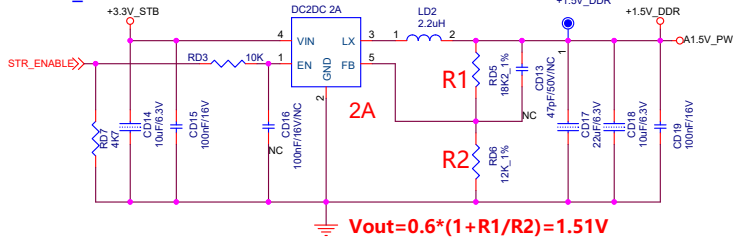
3.3V Standby Power



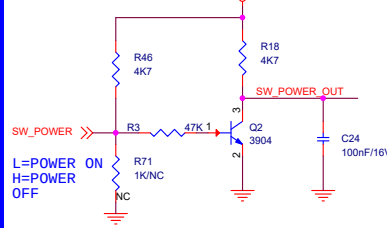
+0.97V_VDDC



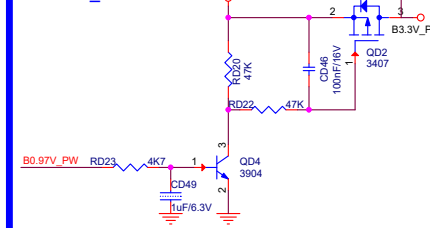
+1.5V_DDR POWER



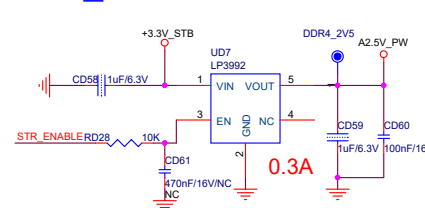
SW



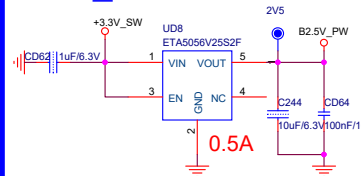
+3.3V_SW



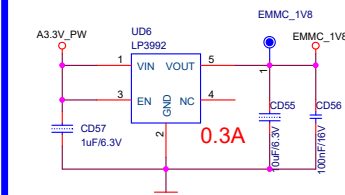
DDR4_2.5V



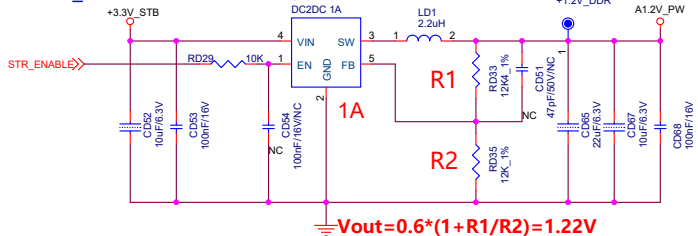
MAIN_2.5V



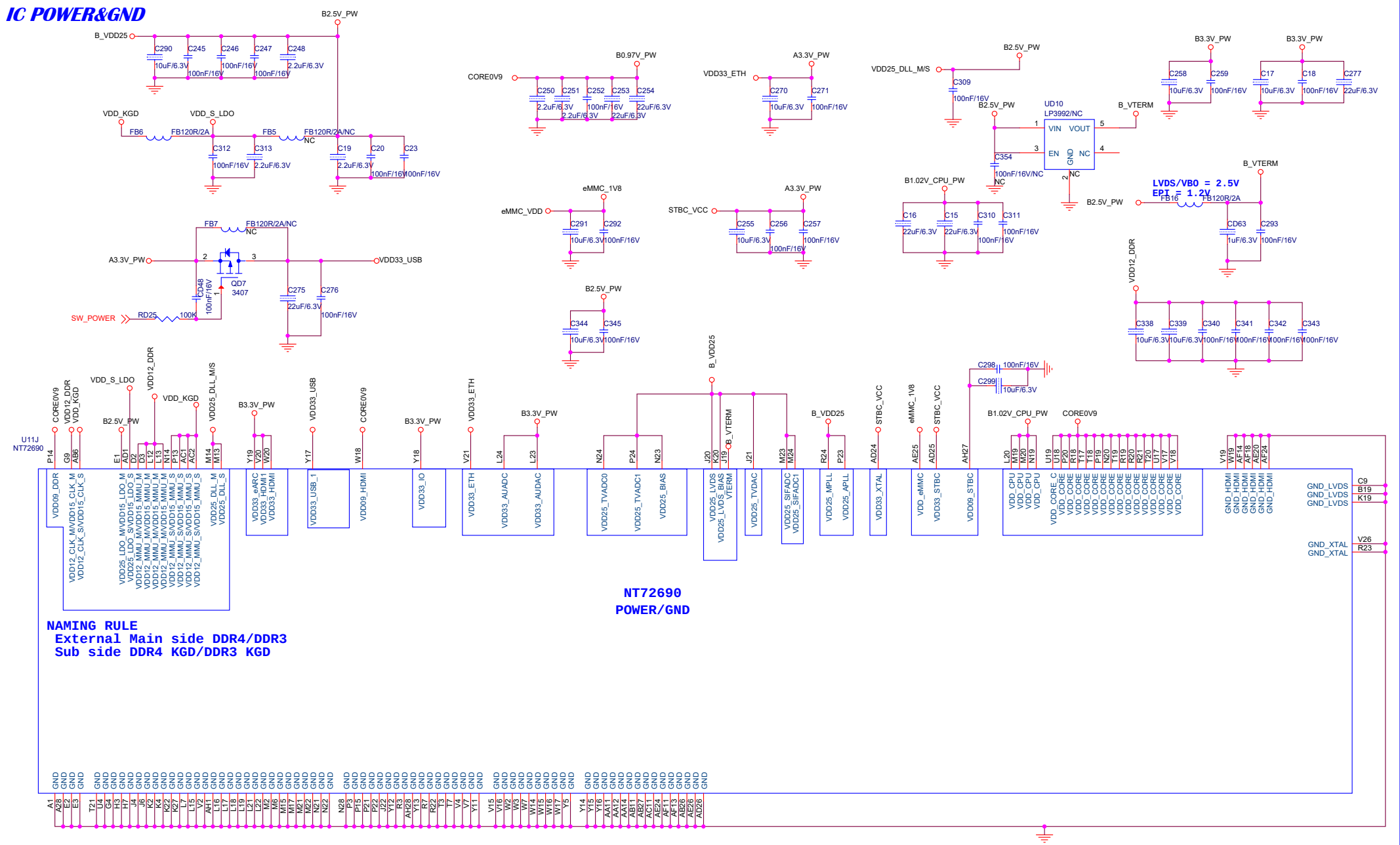
EMMC 1.8V



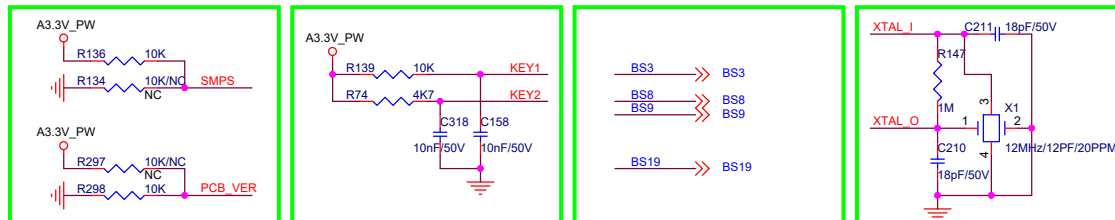
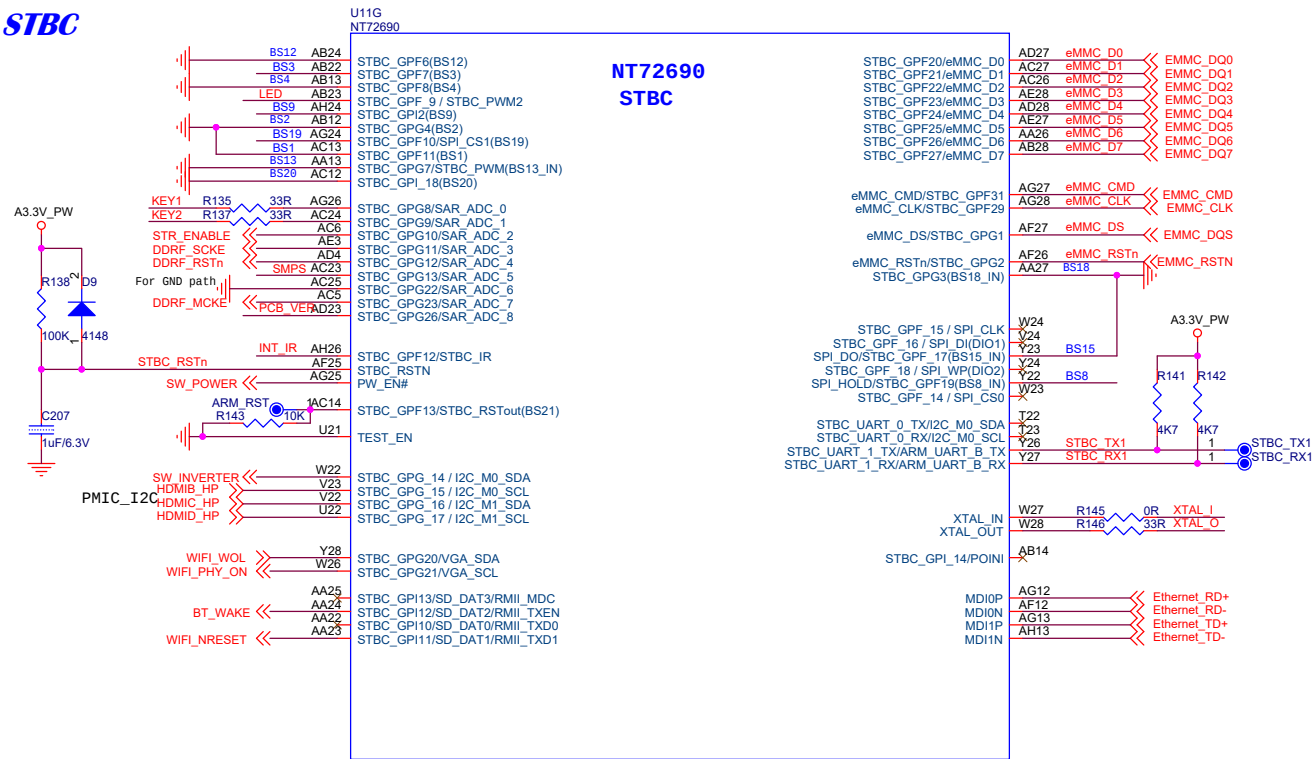
+1.2V_DDR POWER



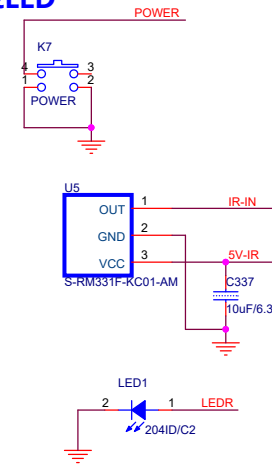
IC POWER&GND



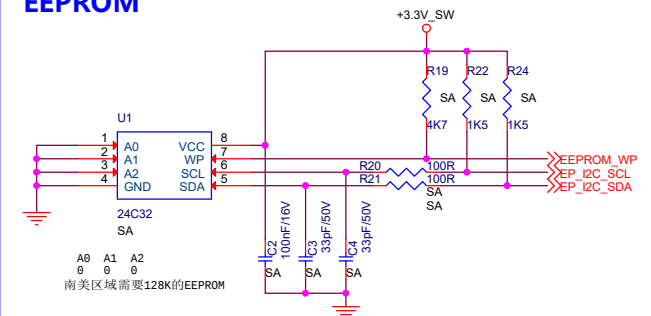
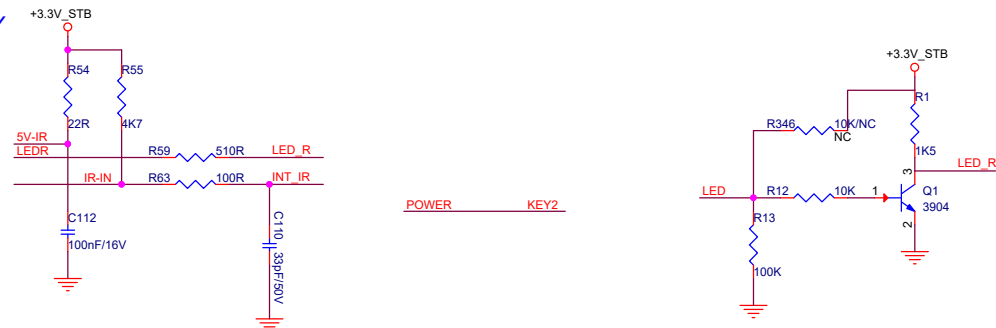
STBC



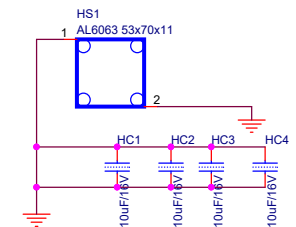
IR&KEY&LED

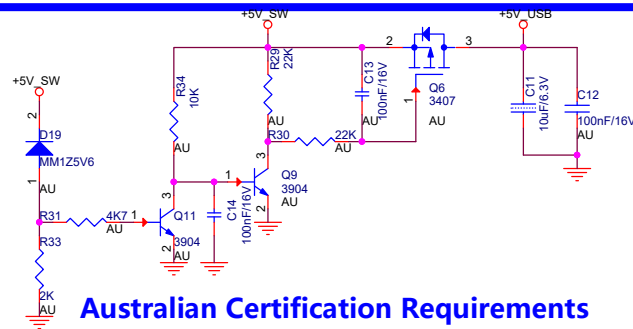
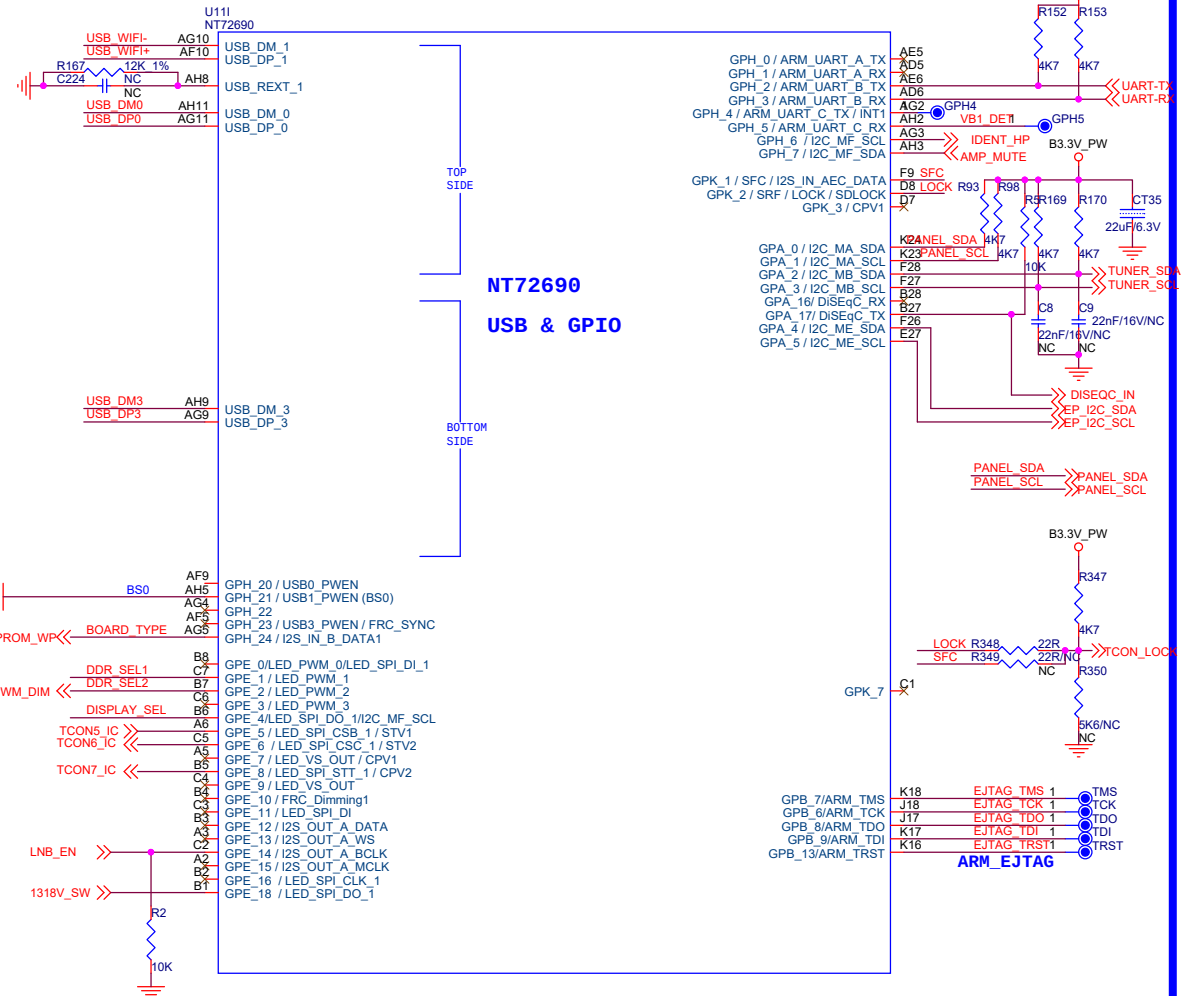


EEPROM

**IR&KEY**

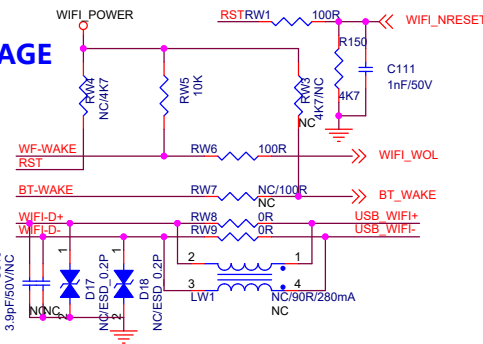
Heat Sink



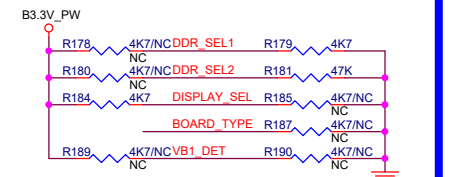
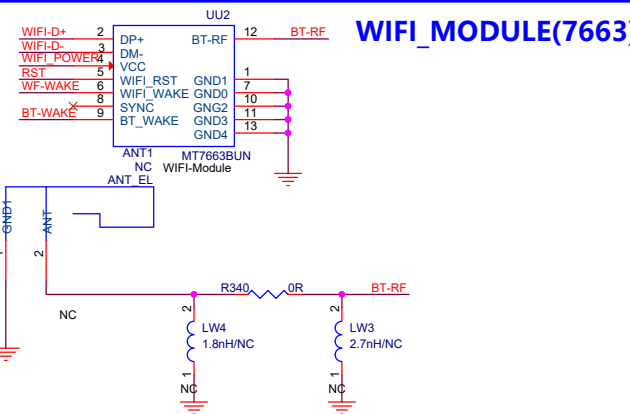
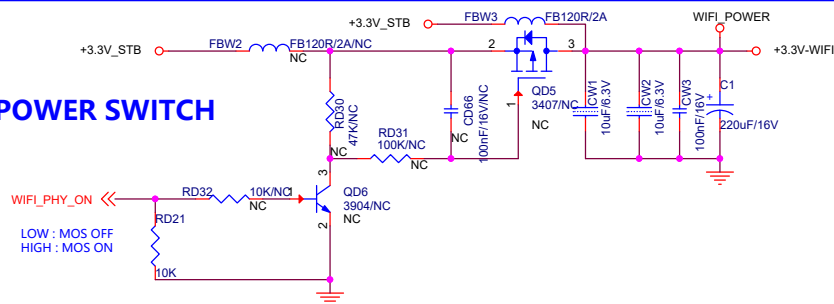


Australian Certification Requirements

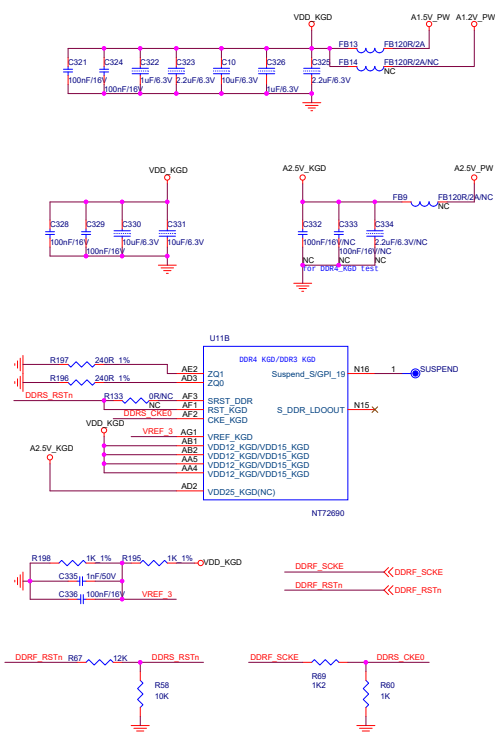
SIGNAL OFFPAGE



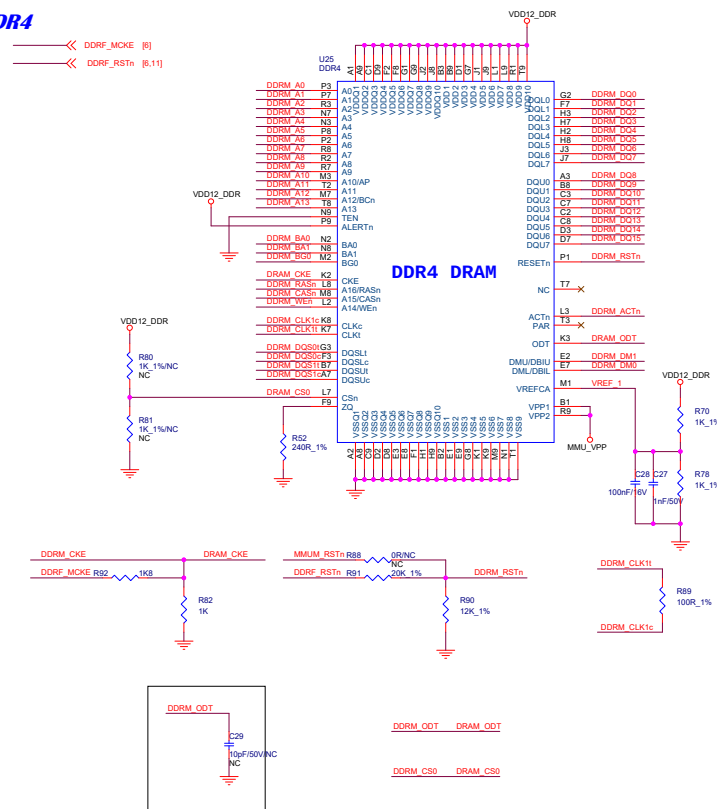
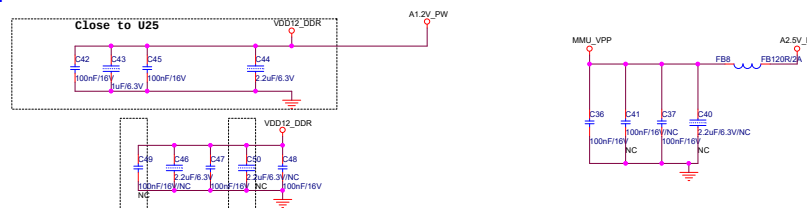
POWER SWITCH

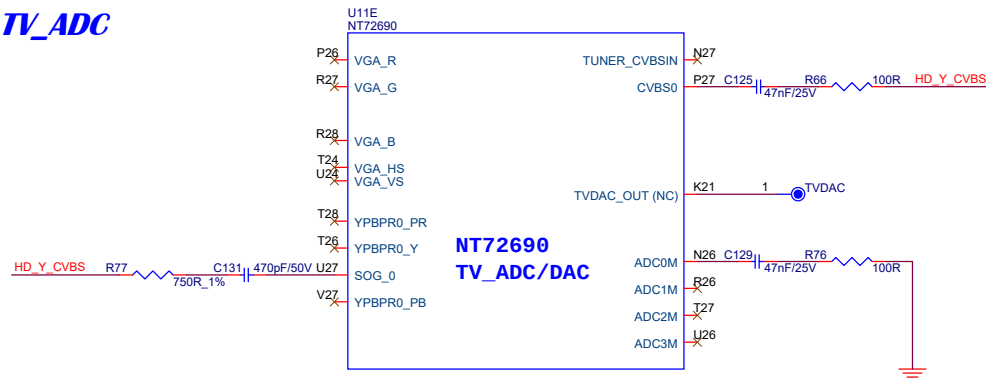
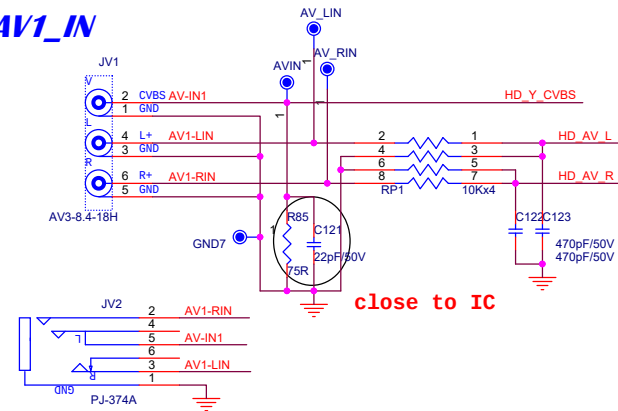
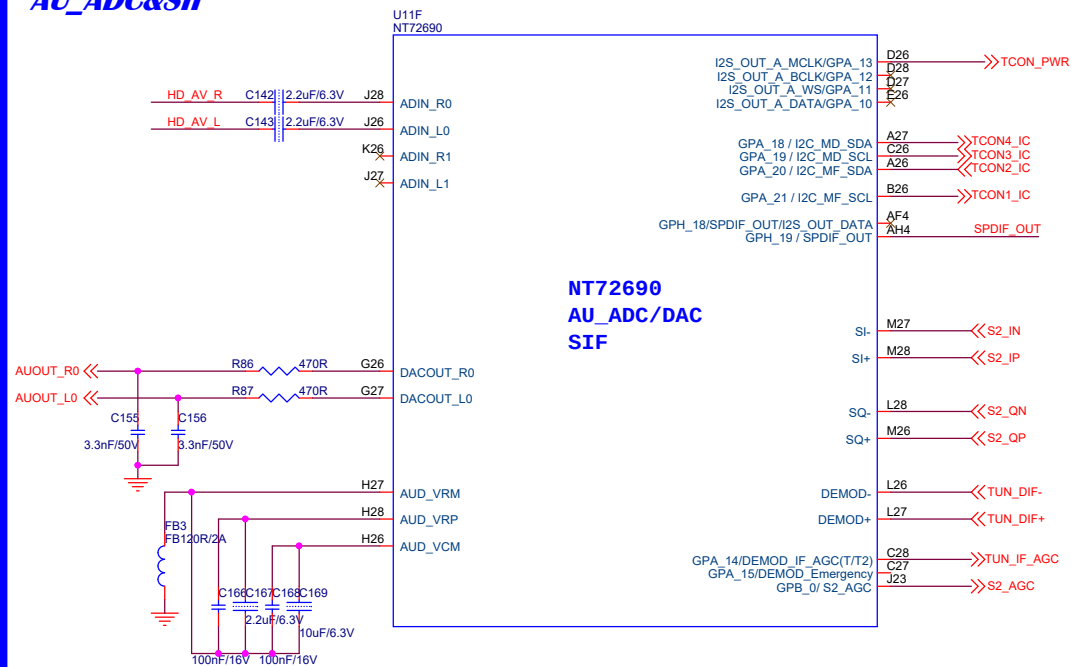


DDR_S

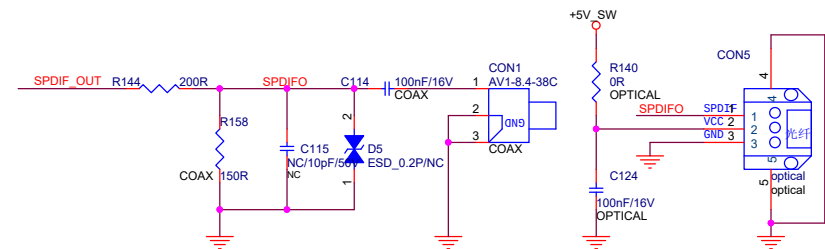


DDR_M

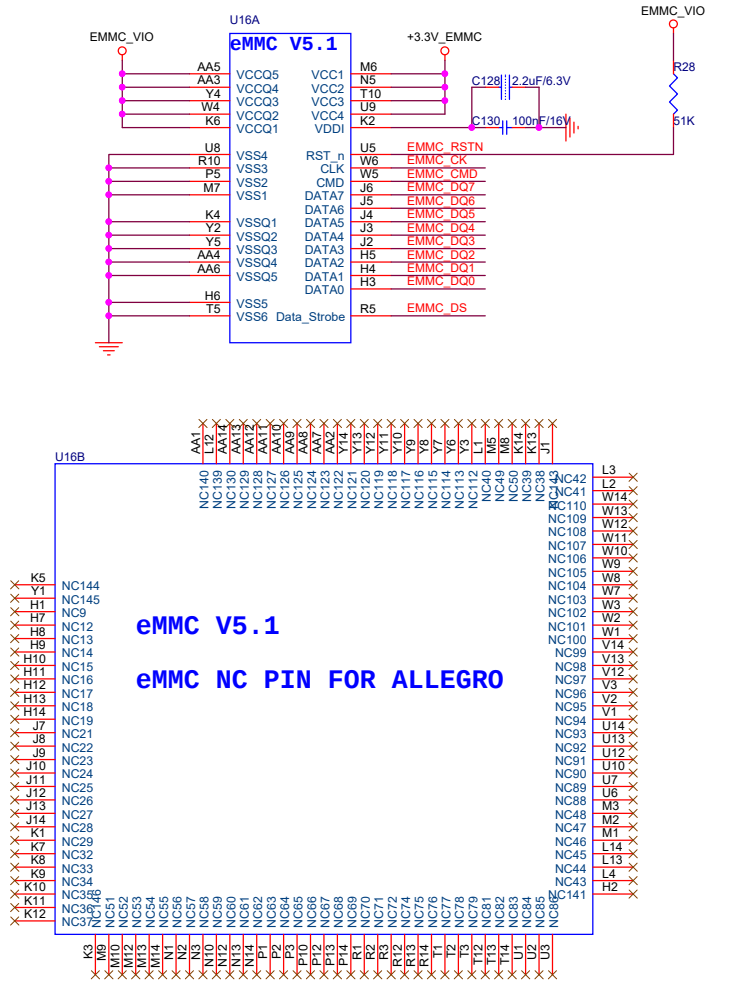
**DDR4****DDR4_POWER**

TV_ADC**AV1_IN*****AU_ADC&SIF***

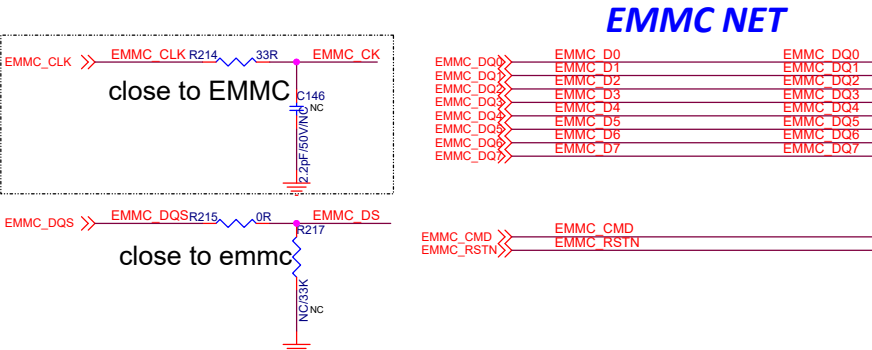
SPDIF OUT



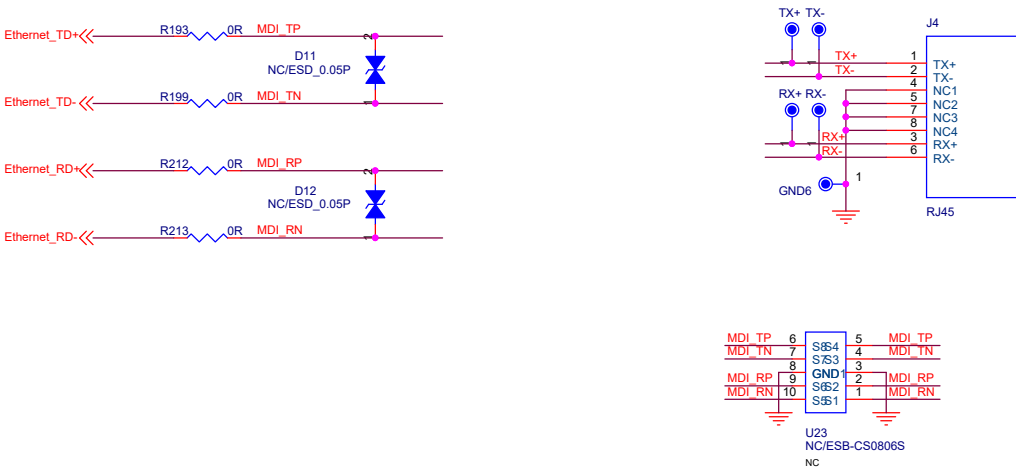
EMMC



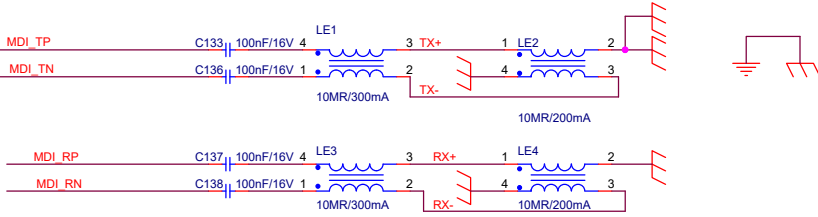
EMMC V5.1
EMMC NC PIN FOR ALLEGRO



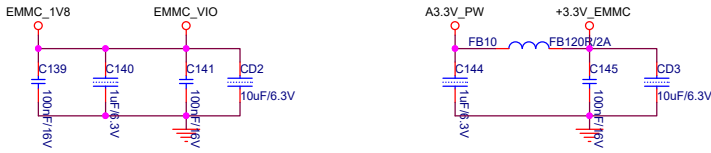
Ethernet Connetor



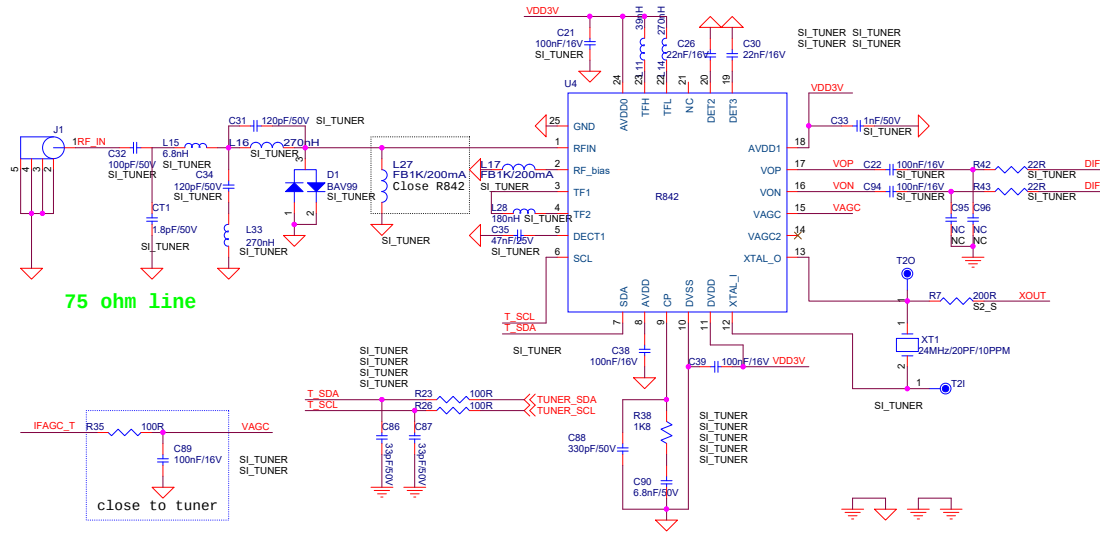
ENTHNET 分立



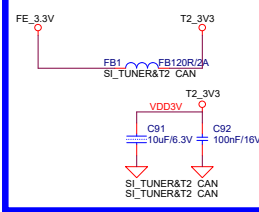
EMMC Power



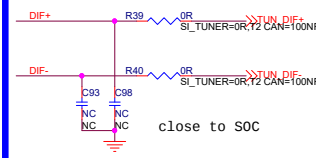
T/T2/C TUNER



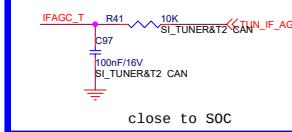
T2 Tuner Power



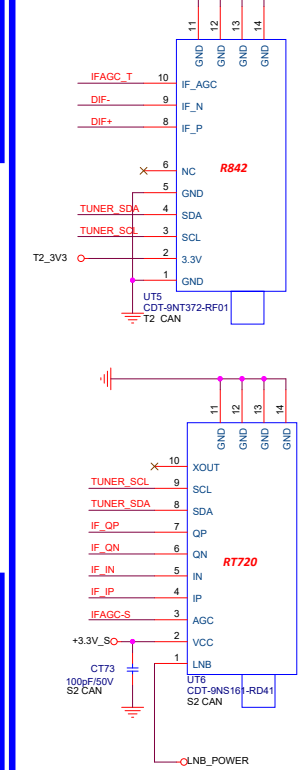
T2_IF



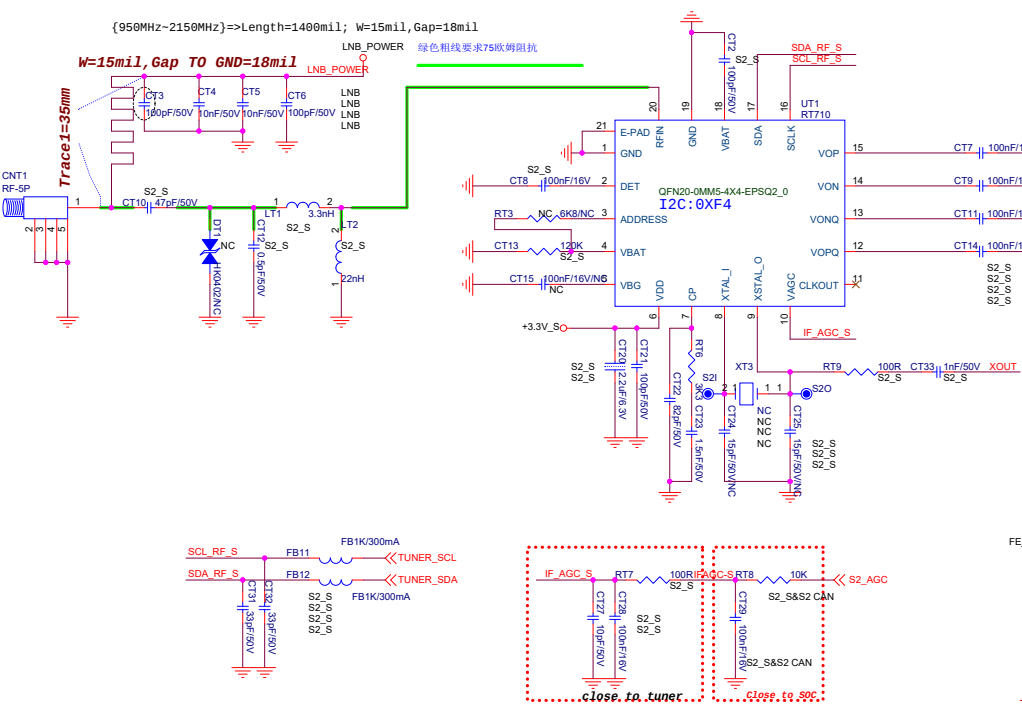
T2_AGC



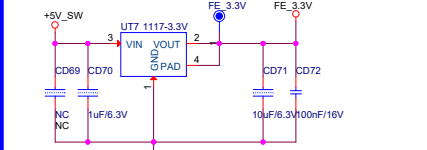
CAN TUNER



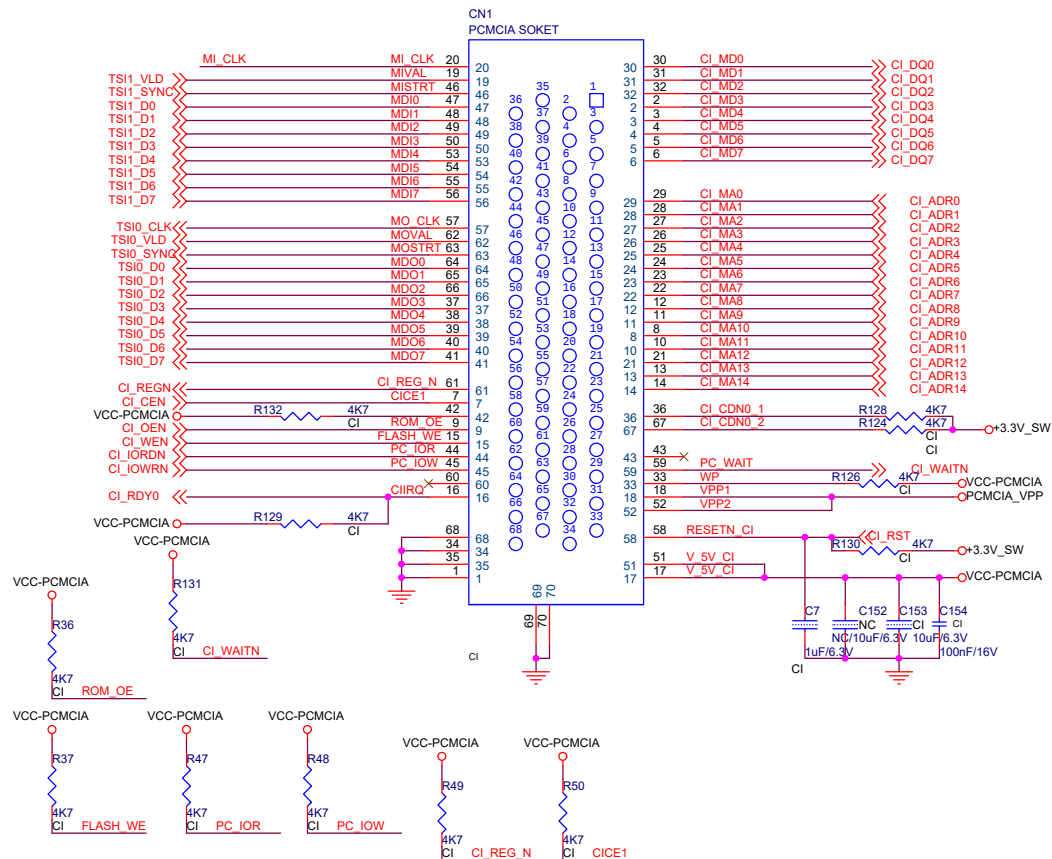
S2 TUNER



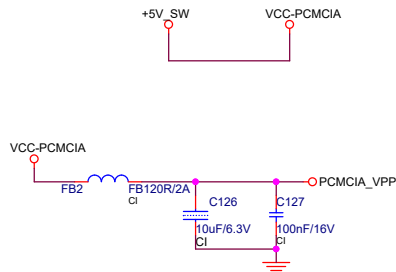
Tuner Power



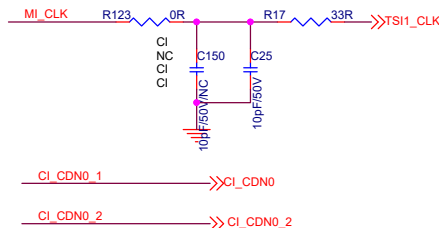
CI Socket



PCMCIA POWER



CLK



AMPLIFY

Diagram illustrating the circuit for the AMPLIFY function, showing the connection of the HAA3603 op-amp (UA1) to various input and output pins.

Key Components and Connections:

- Inputs:** AMP-EN, AMP LIN, AMP REF, AMP_POWER, AMP_RIN, AMP_RINP, AMP_RINNT, AMP_RINNT+, AMP_RINNT-.
- Outputs:** AMP_ROUT+, AMP_ROUT-, AMP_LOUT-, AMP_LOUT+.
- Resistors:** RA1 (0R), RA2 (100K), RA3 (10R), RA4 (7K5), RA5 (10K).
- Capacitors:** CA1 (10uF/16V), CA2 (1nF/50V), CA3 (100nF/16V), CA4 (220nF/16V), CA5 (220nF/16V), CA6 (220nF/16V), CA7 (1nF/50V), CA8 (1uF/16V), CA9 (220nF/16V), CA10 (1nF/50V), CA11 (220nF/16V), CA12 (1nF/50V), CA13 (1uF/16V), CA14 (1uF/16V), CA15 (220nF/16V), CA16 (1nF/50V), CA17 (220nF/16V), CA18 (220nF/16V), CA19 (1nF/50V), CA20 (100nF/16V), CA21 (10uF/16V).
- Op-Amp (UA1):** HAA3603, pins 1 (SD), 2 (FAULT), 3 (LINP), 4 (LINN), 5 (GAIN0), 6 (GAIN1), 7 (AVCC), 8 (AGND), 9 (GVDD), 10 (PLIMIT), 11 (RINN), 12 (RINP), 13 (NC), 14 (PBTL), 15 (PVCC4), 16 (AMP_POWER), 17 (CA18), 18 (OUTP1), 19 (PGND1), 20 (OUTP2), 21 (BSP2), 22 (BSN1), 23 (OUTN1), 24 (PGND2), 25 (OUTN2), 26 (BSN2), 27 (PVCC2), 28 (PVCC1), 29 (GND).

Notes:

- Close to main chip.
- AMP_POWER is connected to pin 16 and pin 17.
- AMP_RINNT+ and AMP_RINNT- are connected to pins 3 and 4.
- AMP_ROUT+ and AMP_ROUT- are connected to pins 1 and 2.
- AMP_LOUT- and AMP_LOUT+ are connected to pins 3 and 4.

Headphone OP

The circuit diagram shows the connection of the APA2172AOL-TRG op-amp for a headphone output. The op-amp is configured as a fully differential amplifier. The non-inverting inputs (RINN and LINN) are connected to the AMP_REF signal. The inverting inputs (RINP and LINP) are connected to the AMP_RIN and AMP_LIN signals through a network of capacitors (CA29, CA30) and resistors (RA14, RA17). The output pins (ROUT and LOUT) are connected to the EAR_ROUT and EAR_LOUT signals through resistors (RA15, RA16). The op-amp is powered by a +12V_STB supply and a +3.3V_SW supply. Various capacitors (CA26, CA27, CA28, CA31, CA32, CA33, CA34, CA35, CA36) and resistors (RA12, RA13, RA18, RA19, RA20) are used for biasing, decoupling, and signal conditioning. The op-amp is also connected to ground (GND) and power ground (PGND) pins.

[illegible]

MUTE

AMP_MUTE
0 MUTE
1 NORMAL

AMP_MUTE >>> RA10 4K7 AMP-EN

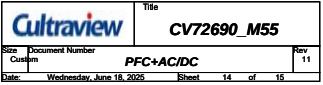
RA11 4K7

4K7

Ground

EARPHONE

Earphone Detect
H: Insert Earphone; GND
L: Pull Out Earphone;



PB-ADJUST >> PB-ADJUST
PB-ON/OFF >> PB-ON/OFF

